

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

INTEGRATED CIRCUIT DESIGN FOR SIGNAL INTEGRITY,  
AVOIDING WELL PROXIMITY EFFECTS

Application Number : 10/708,715  
Confirmation Number:  
First Named Applicant: Karen Bard  
Attorney Docket Number: BUR920030172US1  
Art Unit: 2825  
Examiner: LIN, SUN JAMES  
Search string: ( 5875117 or 20030145296 ).pn

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
931	1	5875117	1999-02-23	Jones et al.	A	716	14

## US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
931	1	20030145296	2003-07-31	Chandra et al.	A1	716	5

## Signature

Examiner Name	Date
James Sun	11-15-05